

IN THE U.S. PATENT & TRADEMARK OFFICE

By electronic communication

<u>First Named Inventor: Thomas J Heller JR :</u>	<u>GROUP ART UNIT 2187</u>
<u>Pat Appln: Serial No. 10/603252:</u>	<u>Examiner: Jared Ian Rutz</u>
<u>Filed: June 25, 2003 :</u>	<u>Date: April 21, 2008</u>
<u>Title: Multiprocessor System with Dynamic Cache Coherency Regions :</u>	
<u>Attorney Docket POU920030046US1 :</u>	<u>Confirmation Number: 3685</u>
<u>Deposit Account: 09-0463 :</u>	Lynn L. Augspurger, Attorney, Reg No. 24,227 :
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AMENDMENT (Responsive to OA Dated 1/09/2008)

Commissioner for Patents
PO Box 1450
Alexandria Virginia, 22313-1450

Sir:

A. Introductory Comments:

In reply to the Official action mailed 1/09/2008 Applicants respectfully request entry of this Amendment to place the application in condition for allowance in light of the remarks and these amendments. (The Commissioner is also hereby authorized to charge payment of the fees including extension of time associated with this communication or credit any overpayment to Deposit Account No. 09-0463. Those expected are already part of the separate fee authorization entry for this transmittal.)